



Everspin Technologies to Chair New JEDEC® Task Group Exploring LPDDR Persistent Memory Standard

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New effort brings together industry stakeholders to address controller support and interoperability for persistent memory within LPDDR architectures

CHANDLER, Ariz.--(BUSINESS WIRE)--Oct. 20, 2026-- Everspin Technologies, Inc. (NASDAQ: MRAM), the world's leading developer and manufacturer of Magnetoresistive Random Access Memory (MRAM) solutions, today announced that Steffen Hellmold, vice president of corporate strategy, is serving as chair of the JEDEC task group focused on exploring a new LPDDR persistent memory (PMEM) standard.

The task group was established under JEDEC's JC-42.6 Low Power Memories Subcommittee following expressions of support from several participating member companies. The proposed effort is intended to explore how persistent memory could leverage the existing LPDDR ecosystem while limiting changes to LPDDR DRAM standards and host controllers.

The work is intended to enable controller support for both DRAM and persistent memory, as well as approaches that enable LPDDR DRAM and persistent memory from multiple vendors to operate within compatible system architectures.

Steffen Hellmold, vice president of corporate strategy at Everspin Technologies and chair of the new JEDEC task group, has participated in JEDEC standards activities for more than 30 years. His career spans product development, technology strategy, business development and corporate strategy across the semiconductor, memory and data storage industries.

"I'm excited to support the JEDEC LPDDR Persistent Memory Task Group as it evaluates requirements and possible standards approaches for persistent memory within the LPDDR ecosystem," Hellmold said. "Everspin brings more than 10 years of experience shipping DDR STT-MRAM for data center applications, giving us a practical understanding of how to integrate persistent memory into customer systems. We look forward to contributing that experience and expertise to the task group's work."

The task group brings together participants from across the semiconductor, memory, storage, controller and hyperscale data center ecosystem. Companies interested in participating can find information about JEDEC membership and the LPDDR PMEM effort on the JEDEC website at <https://www.jedec.org/join-jedec>.

About Everspin Technologies

Everspin Technologies Inc. is the world's leading provider of Magnetoresistive RAM (MRAM). Everspin MRAM delivers the industry's most robust, highest-performance, non-volatile memory for Industrial IoT, Data Centers, and other mission-critical applications where data persistence is paramount. Headquartered in Chandler, Arizona, Everspin provides commercially available MRAM solutions to a large and diverse customer base. For more information, visit www.everspin.com

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This press release contains forward-looking statements regarding future events or results. Forward-looking statements are identified by words such as "will," "expects" or similar expressions and include, but are not limited to, statements regarding Everspin's anticipated business plans and business strategy. These forward-looking statements are subject to known and unknown risks, uncertainties, assumptions, and other factors that may cause actual results or outcomes to be materially different from any future results or outcomes expressed or implied by the forward-looking statements, including, without limitation, the risks set forth under the caption "Risk Factors" in Everspin's Annual Report on Form 10-K for the year ended December 31, 2025 filed with the SEC on March 5, 2026, as well as in Everspin's subsequent filings with the SEC. Any forward-looking statements made by Everspin in this press release speak only as of the date on which they are made, and subsequent events may cause these expectations to change. Everspin disclaims any obligations to update or alter these forward-looking statements in the future, whether as a result of new information, future events or otherwise, except as required by law.

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